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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Kenichi ADACHI

Application No.: 10/575,042

Filing Date: April 5, 2006

For: Task Management Method, Task Management
Device, Semiconductor Integrated Circuit,
Electronic Apparatus, And Task Management
System

Group Art Unit: Not Yet Assigned

Examiner: Not Yet Assigned

Attorney Docket No.: 545/71

INFORMATION DISCLOSURE STATEMENT

Mail Stop: Amendment
Commissioner of Patents
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☒ By checking this paragraph, this Information Disclosure Statement is being filed pursuant to 37 C.F.R. § 1.97(b) within three months of the filing date of the application or before the application has received a substantive Office Action. Accordingly, no fee is necessary; however, should the Patent Office determine that a fee is due, the Commissioner is authorized to charge any such fee to Deposit Account No. 11-0223.

☐ By checking this paragraph, this Information Disclosure Statement is being filed pursuant to 37 C.F.R. § 1.97(c) before the mailing date of a Final Action, a Notice of Allowance, or close of prosecution. Accordingly included herewith is:

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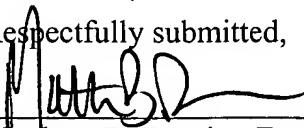
Dated: May 18, 2006 Signature: Ute H. Wojtkowski Printed Name: Ute H. Wojtkowski

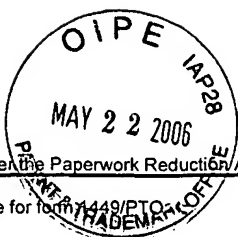
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Dated: May 18, 2006

Respectfully submitted,
By: 
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PTO/SB/08B (07-05)

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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

Complete if Known

Application Number	10/575,042
Filing Date	Herewith
First Named Inventor	Kenichi Adachi
Art Unit	Not Yet Assigned
Examiner Name	Not Yet Assigned
Attorney Docket Number	545/71

Sheet 1 of 1

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	AA	Office Action for JP 2004-163649, dated February 21, 2006	✓

Examiner
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